

**A One-Week
Faculty Development Program
(Online mode)**

**on
RFIC Design & Low Power Analog Circuit
Techniques
December 26-30, 2023**

Convener

Dr. K. Yedukondalu
Professor and Head of ECE
& Associate Dean-Academics

Coordinator

Dr. V. Silpa Kesav
Senior Assistant Professor of ECE

Co-Coordinators

Mrs. K. A. Jyostna, Associate Professor of ECE
Mr. R. Ganesh, Associate Professor of ECE
Mr. Karrar Hussein, Associate Professor of ECE
Mr. S. Suraj Siddarth, Assistant Professor of ECE



Organized by

Department of
Electronics and Communication Engineering
CVR COLLEGE OF ENGINEERING
(An Autonomous Institution, NAAC Grade A)
Vastunagar, Mangalpalli (V), Ibrahimpatan (M)
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Dr G. Ravi Shankar Reddy, Professor of ECE

Dr. P. Srinivasa Rao, Professor of ECE

Dr. S. Praveen Chakravarthy, Professor of ECE

Registration Fees:- Rs 200 /- only

(gpay no- 9866675263)

Registration Link:

<https://forms.gle/GGSy9XpfQM3O4gKN6>

Commencement of registration:

10 December 2023

Last date for registration:

25 December 2023

About the Institution



The Cherabuddi Education Society aims at creating a state-of-the-art Engineering Institution in association with leading NRI technology professionals and well-known academicians of the twin states of Telangana & Andhra Pradesh. The CVR College of Engineering (CVRCE), which was established in 2001, is a pioneer in providing quality education to students. CVRCE, true to its vision is in Pursuit of Excellence while catering to the changing needs of the student community at large for the last couple of decades. CVRCE is ranked among the top few premier institutions in the state of Telangana and holds NIRF ranking less than 200. The College is Autonomous under the UGC and accredited by the NAAC with Grade 'A'. College promotes academic research and consultancy. The college publishes "CVR Journal of Science & Technology" to disseminate research content. The College is working towards accomplishment of its mission to serve the nation as a center of quality education, research, and knowledge.

About ECE Department

The Department of Electronics and Communication Engineering started in 2001. Presently it offers B. Tech. programme with an intake of 120, two M. Tech. programs with specializations in VLSI System Design, and Embedded Systems, with an intake of 18 each.

The B. Tech. ECE Programme is currently accredited by the NBA under Tier I Institutions for 6 years from 2022. M.Tech. (VLSI System Design) and M.Tech. (Embedded Systems) programmes are accredited for three years by the NBA under Tier I in 2022 and 2023 respectively. The ECE Department has 47 Faculty Members and 1 Professor Emeritus. Among faculty, **20** are Doctorates and **23** faculty members are pursuing part time Ph.D. in various other universities.

The Department has well-equipped state-of-the-art laboratories and a dedicated Research Centre and Project Lab facility for UG and PG students. JNTUH accorded Research Center to ECE department, three full-time Ph.D. scholars are working for their doctorates. The Department of ECE has received 1.85 crores of funds till now from external agencies such as UGC, AICTE, DST, DRDO, ECIL, NRSC for research activities.

For any queries, please contact:

Mrs. K. A. Jyostna, Ph:9866675263
Mr. Karrar Hussein, Ph:9705766022

About the Programme

This FDP aims to equip participants with comprehensive knowledge and practical skills in RFIC design principles and low-power analog circuit techniques. Through a series of lectures, hands-on sessions, and discussions, participants will delve into various facets of RF circuits, system exploration, and innovative design considerations. The program will emphasize practical applications and encourage research-oriented thinking to foster innovation in the field.

Objectives

- Designing of Analog Circuits
- Filter Design Concepts
- Encouraging Research in Low Power VLSI & RF Systems
- Proficiency in & RF Transceiver Circuits
- Addressing Non-linearity Challenges

Programme Structure

- Designing of Analog circuits – Differential Opamp, Bandgap Voltage Reference circuits
- Filter Design Concepts
- RF Basics, RF Transceiver circuits
- Non-Linearity in RF circuits- Noise Figure, Dynamic Range etc.
- EDA Tools for Analog & RF circuits
- Low Power VLSI Techniques

Resource Persons

- 1) Shravan Kumar Donthula, Scientist-E, RCI
- 2) Shusma Poreddy, Scientist-E, RCI
- 3) Dr.V.Silpa Kesav, Senior Assistant Professor
- 4) Mrs. K. A. Jyostna, Associate Professor
- 5) Mr. R. Ganesh, Associate Professor
- 6) Mr. Karrar Hussein, Associate Professor

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REGISTRATION FORM

Name

Designation

Qualification

Institution Name

Mobile Number

Email

Any other information:

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Place:

Date:

Signature of the Applicant